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February 17, 2000

BOX PATENT APPLICATION  
Assistant Commissioner for Patents  
Washington, D.C. 20231

Re: Motoya IWASAKI  
CDMA RECEIVER  
Our Ref. Q57879

Dear Sir:

Attached hereto is the application identified above including 15 sheets of the specification, claims, 4 sheets of drawings, executed Assignment and PTO 1595 form, and executed Declaration and Power of Attorney. Also enclosed is the Information Disclosure Statement with form PTO-1449 and references.

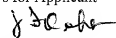
The Government filing fee is calculated as follows:

Total claims	7 - 20	=		x	\$18.00	=	\$0.00
Independent claims	2 - 3	=		x	\$78.00	=	\$0.00
Base Fee							\$690.00
<b>TOTAL FILING FEE</b>							<b>\$690.00</b>
Recordation of Assignment							\$40.00
<b>TOTAL FEE</b>							<b>\$730.00</b>

Checks for the statutory filing fee of \$690.00 and Assignment recordation fee of \$40.00 are attached. You are also directed and authorized to charge or credit any difference or overpayment to Deposit Account No. 19-4880. The Commissioner is hereby authorized to charge any fees under 37 C.F.R. §§ 1.16 and 1.17 and any petitions for extension of time under 37 C.F.R. § 1.136 which may be required during the entire pendency of the application to Deposit Account No. 19-4880. A duplicate copy of this transmittal letter is attached.

Priority is claimed from February 19, 1999 based on Japanese Application No. 041427/1999. The priority document is enclosed herewith.

Respectfully submitted,  
SUGHRUE, MION, ZINN,  
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Attorneys for Applicant

By:   
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## CDMA RECEIVER

### BACKGROUND OF THE INVENTION

#### 1. Field of the Invention:

5           The present invention relates to a receiver, and more particularly to a receiver for use in a CDMA (Code Division Multiplex Access) communication system.

#### 2. Description of the Related Art:

Receivers for use in broadband CDMA communication  
10       systems which are expected to be next-generation portable telephone system standards should comprise RAKE receivers for removing multipath fading interference.

In a CDMA communication system, as shown in Fig. 1 of the accompanying drawings, multiplier 102 in  
15       transmitter 101 spreads transmission data  $x(t)$  by multiplying it by spreading code  $c(t)$ , and the transmitter 101 transmits the spread data as transmission signal  $s(t)$ .

The transmission signal  $s(t)$  transmitted from the  
20       transmitter 101 is subjected to multipath fading over a plurality of transmission paths 103-1 through 103-N with respective delays  $\tau_1 - \tau_N$ . Thereafter, the signal is received as a reception signal  $r(t)$  by a RAKE receiver 104.

25       Specifically, over the respective transmission paths 103-1 - 103-N, the transmission signal  $s(t)$  is

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given the respective delays  $\tau_1 - \tau_N$ , and the results are multiplied by respective coefficients  $a_1 - a_N$  representing respective phase/amplitude ratios of the transmission paths. The signals from the transmission  
5 paths 103-1 through 103-N are then added together, producing the reception signal  $r(t)$ .

As shown in Fig. 2 of the accompanying drawings, the RAKE receiver 104 has a plurality of fingers comprising a plurality of delay units 110-1 - 110-N for  
10 giving delays  $\tau_1 - \tau_N$  depending on the respective transmission paths to the reception signal  $r(t)$ , a plurality of multipliers 120-1 - 120-N for multiplying signals which are produced by delaying the reception  
signal  $r(t)$  for the delays  $\tau_1 - \tau_N$  with the delay units  
15 110-1 - 110-N, by a complex conjugate value  $c(t)^*$  of  $c(t)$ , and outputting product signals, a plurality of integrators 130-1 - 130-N for integrating the product signals from the multipliers 120-1 - 120-N for the period  
of one symbol, and outputting integrals  $f_{1,n} - f_{N,n}$ ,  
20 respectively, a plurality of multipliers 140-1 - 140-N for multiplying the integrals  $f_{1,n} - f_{N,n}$  outputted from the integrators 130-1 - 130-N by complex conjugate values  
 $a_1^* - a_N^*$  of the respective coefficients  $a_1 - a_N$ , and outputting product signals, and an adder 150 for adding  
25 the product signals from the multipliers 140-1 - 140-N and outputting the sum as an output signal series  $R_n$ .

The reception signal  $r(t)$  should ideally be branched into as many as signals as the number of transmission paths, but may not be so branched because of limitations on the circuit scale.

5        Multiplying the integrals  $f_{1,n} - f_{N,n}$  outputted from the integrators 130-1 - 130-N by the complex conjugate values  $a_1^* - a_N^*$  with the multipliers 140-1 - 140-N is equivalent to correcting carrier phase differences caused between the transmission paths over the transmission  
10    paths and weighting the signals depending on amplitude differences between the transmission paths. Therefore, when the signals outputted from the multipliers 140-1 - 140-N are added by the adder 150, their vectors can be combined for a maximum S/N ratio.

15        A process of transmitting and receiving data in the above CDMA communication system will be described below in specific detail with reference to Figs. 1 and 2.

20        The transmission signal  $s(t)$  transmitted from the transmitter is produced by multiplying the transmission data  $x(t)$  by the spreading code  $c(t)$ , as shown by the following equation (1):

$$s(t) = c(t)x(t) \quad \dots(1)$$

25        The transmission data  $x(t)$  is represented by a signal where the value of a transmission data series  $x_n$  continues for a symbol interval  $T$ , as shown by the following equation (2):

$$x(t) = x_n \cdots nT \leq t < (n+1)T \quad \cdots(2)$$

The reception signal  $r(t)$  which has been subjected to multipath fading is produced by giving the respective delays  $\tau_i$  of the transmission paths to the transmission signal  $s(t)$ , multiplying the results by the respective coefficients  $a_i$  representing phase/amplitude ratios of the respective transmission paths, and adding the signals from all the transmission paths, producing the reception signal  $r(t)$ , as shown by the following equation (3):

$$r(t) = \sum_{i=1}^N a_i s(t - \tau_i) = \sum_{i=1}^N a_i c(t - \tau_i) x(t - \tau_i) \quad \cdots(3)$$

In the fingers of the receiver, the delay units 110-1 - 110-N give the reception signal  $r(t)$  the delay times  $\tau_i$  of the transmission paths, as shown by the equation (4) below. Then, the multipliers 120-1 - 120-N multiplies the signals from the delay units 110-1 - 110-N by the complex conjugate value  $c(t)^*$  of the spreading code. Thereafter, the integrators 130-1 - 130-N integrate the product signals from the multipliers 120-1 - 120-N for the period of one symbol, producing integrals  $f_{j,n}$ . As shown by the equation (5) below, the integrals  $f_{j,n}$  outputted from the integrators 130-1 - 130-N are represented by the sum of the products of the transmission data series  $x_n$  and the phase/amplitude ratios of the transmission paths, and interferences  $I_{j,n}$  composed of signal components from other transmission paths having different delay times.

$$\begin{aligned}
f_{jn} &= \int_{nT}^{(n+1)T} r(t + \tau_j) c^*(t) dt = \int_{nT}^{(n+1)T} \sum_{i=1}^N a_i c(t - \tau_i + \tau_j) x(t - \tau_i + \tau_j) c^*(t) dt \dots (4) \\
&= a_j \int_{nT}^{(n+1)T} x(t) dt + \sum_{i=1}^{j-1} a_i \int_{nT}^{(n+1)T} c^*(t) c(t - \tau_i + \tau_j) x(t - \tau_i + \tau_j) dt \\
&= a_j x_n + I_{jn} \dots (5)
\end{aligned}$$

5 Then, the multipliers 140-1 - 140-N multiply the integrals  $f_{j,n}$  outputted from the integrators 130-1 - 130-N by the complex conjugate values  $a_j^*$  of the phase/amplitude ratios of the transmission paths.

Thereafter, the adder 150 adds the outputs from the  
10 fingers together, and outputs the sum as the output signal series  $R_n$ , as shown by the equations (6), (7) below. When the multipliers 140-1 - 140-N multiply the integrals  $f_{j,n}$  by the complex conjugate values  $a_j^*$  of the phase/amplitude ratios, the phase errors of the  
15 respective paths are corrected, and the signals are weighted for a maximum S/N ratio.

$$R_n = \sum_{i=1}^N a_i^* f_{in} = \sum_{j=1}^N |a_j|^2 x_n + \sum_{j=1}^N a_j^* I_{jn} \dots (6)$$

$$= x_n \sum_{j=1}^N |a_j|^2 + \sum_{j=1}^N a_j^* I_{jn} \dots (7)$$

20 The RAKE receiver tends to have a large circuit scale because it needs to have a plurality of despanders parallel to each other for despreading the reception signal.

However, terminals which have the RAKE receiver are  
25 required to have a small circuit scale in view of demands for a low price and power requirements.

If terminals have a large circuit scale due to the incorporation of the RAKE receiver, then the price and power requirements of the terminals cannot be reduced.

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#### SUMMARY OF THE INVENTION

It is therefore an object of the present invention to provide a CDMA receiver which has a relatively small circuit scale, and yet is capable of performing functions equivalent to those of the conventional CDMA receivers.

10

In a receiver according to the present invention, a plurality of delay means add delays over transmission paths to a complex conjugate value of a spreading code used when a reception signal is transmitted, and output delayed signals. A plurality of multiplying means

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multiply the delayed signals outputted from the delay means by complex conjugate values of coefficients representing respective phase/amplitude ratios of the transmission paths, and output product signals. An adding means adds the product signals outputted from the multiplying means, and outputs the sum as a code for despread the reception signal. A despread means despreads the reception signal using the code outputted from the adding means.

20

Since the code for despread the reception signal is calculated on the basis of the delays given to the reception signal over transmission paths and the

coefficients representing the respective phase/amplitude ratios of the transmission paths, and the reception signal is despread using the calculated code, it is not necessary to provide as many despreading circuits as the number of the transmission paths. Therefore, the receiver according to the present invention is reduced in circuit scale.

The above and other objects, features, and advantages of the present invention will become apparent from the following descriptions with reference to the accompanying drawings which illustrate examples of the present invention.

#### BRIEF DESCRIPTION OF THE DRAWINGS

Fig. 1 is a block diagram illustrative of a process of transmitting data in a CDMA communication system;

Fig. 2 is a block diagram of a RAKE receiver;

Fig. 3 is a block diagram of a receiver according to an embodiment of the present invention; and

Fig. 4 is a block diagram of a simplified representation of the receiver shown in Fig. 3.

#### DETAILED DESCRIPTION OF THE PREFERRED EMBODIMENT

As shown in Fig. 3, a receiver according to an embodiment of the present invention comprises a despreading code calculator 1 for calculating a



despreading code  $c_x(t)$  for despreading a reception signal  $r(t)$  based on delays given to the reception signal  $r(t)$  over a plurality of transmission paths through which the reception signal  $r(t)$  is transmitted and coefficients  
5 representing respective phase/amplitude ratios of the transmission paths, a despreader 20 for despreading the reception signal  $r(t)$  using the despreading code  $c_x(t)$  calculated by the despreading code calculator 1, and an integrator 30 for integrating the reception signal  
10 despread by the despreader 20 and outputting the integrated signal as a reception signal series  $R_n$ .

The despreading code calculator 1 comprises a plurality of delay units 10-1 - 10-N for adding delays  $\tau_1$  -  $\tau_N$  over the transmission paths through which the  
15 reception signal  $r(t)$  is transmitted, to complex conjugate value  $c(t)^*$  of the spreading code, and outputting delayed signals, a plurality of multipliers 40-1 - 40-N for multiplying the delayed signals outputted from the delay units 10-1 - 10-N by complex conjugate  
20 values  $a_1^*$  -  $a_N^*$  of coefficients  $a_1$  -  $a_N$  by which the transmission signal has been multiplied, and outputting product signals, and an adder 50 for adding the product signals outputted from the multipliers 40-1 - 40-N, and outputting the sum as the despreading code  $c_x(t)$ .

25 In the receiver shown in Fig. 3, the reception signal  $r(t)$  is not branched into as many as signals as

the number of transmission paths for being delayed and corrected in phase. Instead, the delays  $\tau_1 - \tau_N$  over the transmission paths through which the reception signal  $r(t)$  is transmitted are added to the code  $c(t)^*$  used to

- 5 despread the reception signal  $r(t)$  for delaying and correcting the reception signal  $r(t)$  in phase and adding amplitude weights to the reception signal  $r(t)$ .

Thereafter, the delayed signals are multiplied by the complex conjugate values  $a_1^* - a_N^*$  of the coefficients  $a_1$   
10 -  $a_N$  by which the transmission signal has been multiplied over the respective transmission paths, thereby producing the code  $c_x(t)$  for despreading the reception signal  $r(t)$ .

Using the despreding code  $c_x(t)$ , the reception  
15 signal  $r(t)$  is despread into the reception signal series  $R_n$ .

The equation (7) referred to above can be modified into the following equations (8) - (11):

$$R_n = \sum_{j=1}^N a_j \cdot \int_{nT}^{(n+1)T} r(t + \tau_j) c^*(t) dt = \sum_{j=1}^N a_j \cdot \int_{nT + \tau_1}^{(n+1)T + \tau_1} r(t) c^*(t - \tau_1) dt \quad \dots (8)$$

$$20 = \sum_{j=1}^N a_j \cdot \int_{nT}^{(n+1)T + \Delta} r(t) c^*(t - \tau_1) dt = \int_{nT}^{(n+1)T + \Delta} r(t) \sum_{j=1}^N a_j \cdot c^*(t - \tau_1) dt \quad \dots (9)$$

$$= \int_{nT}^{(n+1)T + \Delta} r(t) c_x(t) dt \quad \dots (10)$$

$$c_x(t) = \sum_{j=1}^N a_j \cdot c^*(t - \tau_1) \quad \dots (11)$$

When the equation (9) is expressed by a block form,  
25 the circuit shown in Fig. 3 is achieved.

As described above, rather than despread the reception signal  $r(t)$  into as many signals as the transmission paths and adding the despread signals into the reception signal series  $R_n$ , the code  $c(t)^*$  used to despread the reception signal  $r(t)$  is given the delays over the respective transmission paths, the delayed codes are multiplied by the complex conjugate values representing the phase/amplitude ratios of the transmission paths, and the product signals are added into the code  $c_x(t)$  for despread the reception signal. The reception signal  $r(t)$  is thereafter despread by the code  $c_x(t)$ . The receiver according to the embodiment of the present invention can thus comprise a single finger.

If the desreading code  $c_x(t)$  is calculated in advance and stored in a memory 60, as shown in Fig. 4, then the receiver can be constructed of a single desreader only, and hence can be of a highly simplified circuit arrangement.

In the circuit shown in Fig. 4, the desreading code may be calculated and stored as a table if fading parameters (delay times and phase/amplitude ratios) suffer no time-dependent variations or slow time-dependent variations.

While a preferred embodiment of the present invention has been described using specific terms, such description is for illustrative purposes only, and it is

to be understood that changes and variations may be made without departing from the spirit or scope of the following claims.

What is claimed is:

1. A receiver comprising:

despreading code calculating means for  
calculating a code for despreading a reception signal  
based on delays given to the reception signal over a  
5 plurality of transmission paths through which the  
reception signal is transmitted and coefficients  
representing respective phase/amplitude ratios of the  
transmission paths;

despreading means for despreading the  
10 reception signal using the code calculated by said  
despreading code calculating means; and

integrating means for integrating the  
reception signal despread by said despreading means.

2. A receiver comprising:

memory means for storing a code for  
despreading a reception signal, calculated in advance  
based on delays given to the reception signal over a  
5 plurality of transmission paths through which the  
reception signal is transmitted and coefficients  
representing respective phase/amplitude ratios of the  
transmission paths;

despreading means for despreading the  
10 reception signal using the code stored by said memory  
means; and

integrating means for integrating the  
reception signal despread by said despreading means.

3. A receiver according to claim 1, wherein said  
despreading code calculating means comprises:

a plurality of delay means for adding the  
delays over the transmission paths to a complex conjugate  
5 value of a spreading code used when the reception signal  
is transmitted, and outputting delayed signals;

a plurality of multiplying means for  
multiplying the delayed signals outputted from said delay  
means by complex conjugate values of the coefficients  
10 representing the respective phase/amplitude ratios of the  
transmission paths, and outputting product signals; and

adding means for adding the product signals  
outputted from said multiplying means, and outputting the  
sum as the code for despreading the reception signal.

4. A receiver according to claim 1, further  
comprising memory means for storing the code outputted  
from said despreading code calculating means, said  
despreading means comprising means for despreading said  
5 reception signal using the code stored by said memory  
means.

5. A receiver according to claim 3, further comprising memory means for storing the code outputted from said adding means, said despreading means comprising means for despreading said reception signal using the  
5 code stored by said memory means.

6. A receiver according to claim 3, wherein there are as many said delay means and said multiplying means as the number of the transmission paths.

7. A receiver according to claim 5, wherein there are as many said delay means and said multiplying means as the number of the transmission paths.

# ABSTRACT OF THE DISCLOSURE

A receiver for use in a CDMA communication system, which is designed for a small size, has a plurality of delay units for adding delays over transmission paths to  
5 a complex conjugate value of a spreading code, a plurality of multipliers for multiplying the delayed signals outputted from the delay units by complex conjugate values of coefficients representing respective phase/amplitude ratios of the transmission paths, and an  
10 adder for adding the product signals outputted from the multiplier, and a despreader for despreading a reception signal using the sum signal outputted from the adder.





Fig. 2 (Prior Art)

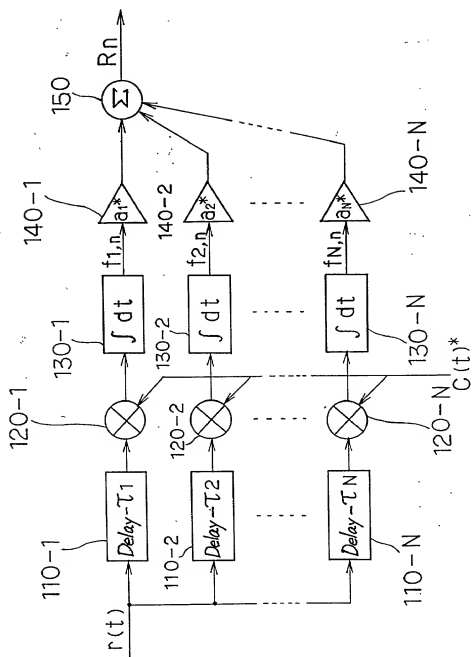


Fig. 3

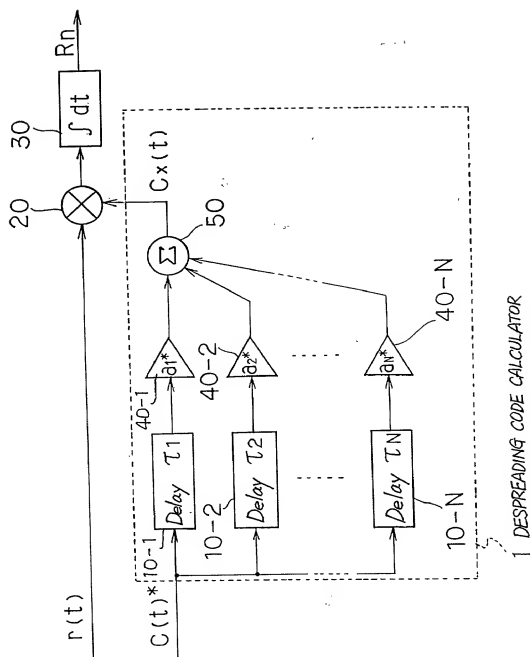
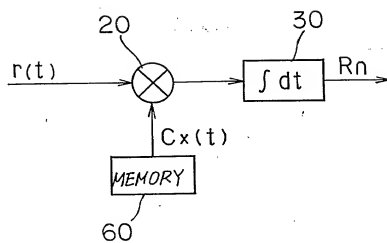


Fig. 4



**DECLARATION AND POWER OF ATTORNEY**

As a below named inventor, I hereby declare that my residence, post office address and citizenship are as stated below next to my name: that I verily believe I am the original, first and sole inventor (if only one name is listed below) or a joint inventor (if plural names are listed below) of the subject matter claimed and for which a patent is sought in the application entitled:

**CDMA RECEIVER**

which application is:

☒ the attached application  
(for original application)

\_\_\_\_\_ application Serial No. \_\_\_\_\_  
filed \_\_\_\_\_, and amended on \_\_\_\_\_

\_\_\_\_\_ (for declaration not accompanying application)

that I have reviewed and understand the contents of the specification of the above-identified application, including the claims, as amended by any amendment referred to above; that I acknowledge my duty to disclose information of which I am aware which is material to the patentability of this application under 37 C.F.R. 1.56, that I hereby claim foreign priority benefits under Title 35, United States Code §119, §172 or §365 of any foreign application(s) for patent or inventor's certificate listed below and have also identified on said list any foreign application for patent or inventor's certificate on this invention having a filing date before that of the application on which priority is claimed:

Application Number	Country	Filing Date	Priority Claimed (yes or no)
041427/1999	Japan	February 19, 1999	yes

I hereby claim the benefit of Title 35, United States Code §120 of any United States application(s) listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in a listed prior United States application in the manner provided by the first paragraph of Title 35, United States Code, §112, I acknowledge my duty to disclose any information material to the patentability of this application under 37 C.F.R. 1.56 which occurred between the filing date of the prior application and the national or PCT international filing date of this application:

Application Serial No.	Filing Date	Status (patented, pending, abandoned)
------------------------	-------------	--

I hereby appoint John H. Mion, Reg. No. 18,879; Donald E. Zinn, Reg. No. 19,046; Thomas J. Macpeak, Reg. No. 19,292; Robert W. Seas, Jr., Reg. No. 21,092; Darryl Mexic, Reg. No. 23,063; Robert V. Sloan, Reg. No. 22,775; Peter D. Olexy, Reg. No. 24,513; J. Frank Osha, Reg. No. 24,625; Waddell A. Biggart, Reg. No. 24,861; Robert G. McMorrow, Reg. No. 19,093; Louis Rubinsky, Reg. No. 24,835; Neil B. Siegel, Reg. No. 25,200; David J. Cushing, Reg. No. 28,703; John R. Inge, Reg. No. 26,916; Joseph J. Ruch, Jr., Reg. No. 26,577; Sheldon I. Landsman, Reg. No. 25,430; Richard C. Turner, Reg. No. 29,710; Howard L. Bernstein, Reg. No. 25,665; Alan J. Kasper, Reg. No. 25,426; Kenneth J. Burchfiel, Reg. No. 31,333; Gordon Kit, Reg. No. 30,764; Susan J. Mack, Reg. No. 30,951; Frank L. Bernstein, Reg. No. 31,484; Mark Boland, Reg. No. 32,197; William H. Mandir, Reg. No. 32,156; Scott M. Daniels, Reg. No. 32,562; Brian W. Hannon, Reg. No. 32,778 and Abraham J. Rosner, Reg. No. 33,276, my attorneys to prosecute this application and to transact all business in the Patent and Trademark Office connected therewith, and request that all correspondence about the application be addressed to **SUGHRUE, MION, ZINN, MACPEAK & SEAS**, 2100 Pennsylvania Avenue, N.W., Washington, D.C. 20037-3202.

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issuing thereon.

Date February 4, 2000  
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